Substitute for form 1449B/PTO	Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Application Number	10/694,919
	Filing Date	October 27, 2003
	First Named Inventor	Hwang, Yean-Yow
	Art Unit	To Be Assigned 2 82 5
(use as many sheets as necessary)	Examiner Name	To Be Assigned TUYEN TO
Sheet 1 of 1	Attorney Docket Number	015114-066600US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T [2]
TT	0001	Cong, J and Ding, Y (1996). "Combinational logic synthesis for (LUT) based field programmable gate arrays," ACM Trans. Des. Autom. Electron. Syst. 1(2):145-204.	
TT	0002	Cong, J. and Hwang, Y. (2001). "Boolean Matching for LUT-Based Logic Blocks With Applications to Architecture Evaluation and Technology Mapping," IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 20(9):1077-1090.	

Examiner Signature	Tuyen To	Date Considered	09/14/2005

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.
[1] Applicant's unique citation designation number (optional). [2] Applicant is to place a check mark here if English language Translation is attached.

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